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To:	Examiner Chun Kuan Lee	From:	Mark J. Rozman, Reg. No. 42,117
Company:	USPTO	Date:	May 20, 2008
Fax:	(571) 273-8300	Pages:	8 including cover sheet
Your Re:	Serial No. 10/806,787	Our Re:	ML1576US (PI 8870)

☐ Urgent    ☒ For Review    ☐ Please Comment    ☐ Please Reply    ☒ Confirm Receipt

## MESSAGE:

The Patent and Trademark Office confirmation sheet sets for the receipt date of the following documents identified as follows:

Applicants: Prashant Sethi, et al.  
Serial No.: 10/806,787  
Filing Date: March 22, 2004  
Title: A Method And System For Configuration Of Processor Integrated Devices In Multi-Processor Systems

1. Reply to Office Action Mailed February 20, 2008 (7 pages)

Date of Deposit:	May 20, 2008
I hereby certify under 37 CFR 1.8 this correspondence is being deposited as a facsimile transmission to (571) 273-8300 on the date indicated above.	
Stephanie Petreas	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Prashant Sethi, et al.	§	Group Art Unit:	2181
		§		
Serial No.:	10/806,787	§		
		§	Examiner:	Chun Kuan Lee
Filed:	March 22, 2004	§		
		§		
For:	A Method And System For Configuration Of Processor Integrated Devices In Multi- Processor Systems	§	Atty. Dkt. No.:	ITL.1576US (P18870)
		§		

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO OFFICE ACTION MAILED FEBRUARY 20, 2008**

Sir:

In response to the Office Action mailed February 20, 2008, please amend the above-referenced patent application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

5/20/08  
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